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Bib Data Sheet

CONFIRMATION NO. 6066

SERIAL NUMBER 10/619,136	FILING DATE 07/14/2003 RULE	CLASS 324	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. X-1269-1P US
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** CONTINUING DATA *****

This application is a CIP of 10/355,563 01/31/2003 PAT 6,788,095

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/08/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	CA	6	26	2

ADDRESS

24309

XILINX, INC

ATTN: LEGAL DEPARTMENT

2100 LOGIC DR

SAN JOSE, CA

95124

TITLE

Method for gross I/O functional test at wafer sort

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